

CLAIMS

1. A method of manufacturing a plasma display panel (PDP) including a scan electrode, a sustain electrode, and an address electrode, comprising a step of: applying to the address electrode at
5 least one of a first pulse voltage for the address electrode and a second pulse voltage for the address electrode, in an aging step in which aging discharge is performed by alternately applying pulse voltage for the scan electrode and pulse voltage for the sustain electrode at least across the scan electrode and the sustain electrode, wherein the first
10 pulse voltage has rising edge timing synchronizing with rising edge timing of the pulse voltage for the scan electrode and a pulse width smaller than that of the pulse voltage for the scan electrode, and the second pulse voltage for the address electrode has rising edge timing synchronizing with rising edge timing of the pulse voltage for the
15 sustain electrode and a pulse width smaller than that of the pulse voltage for the sustain electrode.

2. The method of manufacturing a PDP of claim 1, wherein there is at least one of a period for stopping application of the first
20 pulse voltage for the address electrode to the address electrode and a period for stopping application of the second pulse voltage for the address electrode to the address electrode.

3. The method of manufacturing a PDP of claim 2, wherein the
25 first pulse voltage for the address electrode and the second pulse voltage for the address electrode are applied to the address electrode so that the first pulse voltage is applied less than four times successively and the second pulse voltage is applied less than four

times successively.

4. The method of manufacturing a PDP of any one of claims 1 to 3, wherein values of the first pulse voltage for the address electrode and the second pulse voltage for the address electrode do not exceed a value of the pulse voltage for the scan electrode and a value of the pulse voltage for the sustain electrode.

5. The method of manufacturing a PDP of any one of claims 1 to 3, wherein a value of at least one of the pulse voltage for the scan electrode, the pulse voltage for the sustain electrode, and the pulse voltage for the address electrode is decreased with time.

6. A method of manufacturing a plasma display panel including a scan electrode, a sustain electrode, and an address electrode, comprising the steps of: causing discharge one of between the scan electrode and the address electrode, and the sustain electrode and the address electrode; and using this discharge, triggering discharge between the scan electrode and sustain electrode, in an aging step in which aging discharge is performed by alternately applying pulse voltage for the scan electrode and pulse voltage for the sustain electrode at least across the scan electrode and the sustain electrode.